

AMD Radeon VII Available

Experimental Computing Lab (ExCL) managed by the ORNL Future Technologies Group

- AMD Radeon VII, Vega 20 Architecture
 - GCN 5 on TSMC 7FF process, 13.2B transistors
 - 60 Compute Units with 3.4 DP peak TF
 - 16 GB HBM2 with 4096-bit width for ~1TBps bandwidth
 - TBP 300W
 - PCIe 3.0 x16
- Intel Xeon Skylake Host
 - HP Z4 G4 Workstation w/ PCIe 3.0 x16
 - W-2123 / 64Gb host
 - 1 CPU * 4 cores * 2 threads/core
 - 512 GB SSD uncommitted/available
- Software
 - AMD ROCm development tools
 - HIP (Heterogeneous Compute Interface for Portability) available
 - OpenCL 2.1
- Additional Details
 - <https://www.anandtech.com/show/13832/amd-radeon-vii-high-end-7nm-february-7th-for-699>
 - https://en.wikipedia.org/wiki/AMD_RX_Vega_series#cite_note-anand_radeon_vii

