Intel Stratix 10 FPGA available

Experimental Computing Lab (ExCL) managed by the ORNL Future Technologies Group

- Intel Stratix 10 FPGA and four banks of DDR4 external memory

  - Board configuration: Nallatech 520 Network Acceleration Card

- Up to 10 TFLOPS of peak single precision performance

- 25MBytes of L1 cache @ up to 94 TBytes/s peak bandwidth

- 2X Core performance gains over Arria® 10

- Quartus and OpenCL software (Intel SDK v18.1) for using FPGA

- Provide researcher access to advanced FPGA/SOC environment

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For more information or to apply for an account, visit https://excl.ornl.gov/

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